	Search Text
1	(bus communication) with protocol and port same (designer engineer user developer)
2	(select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
3	number same ((bus communication) with protocol) and port same (designer engineer user developer) same select$6 identif$7
4	(system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
5	port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
6	bus same port same (pld fpga module block component ip device peripheral) and port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
7	(compar$6 match$5) same (bus communication protocol) same (compati$6 common) and bus same port same (pld fpga module block component ip device peripheral) and port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
8	(port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
9	(designer engineer user developer) with (select$5 identif$6 specif$6 input$6 provid$6) and (port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
10	(common compati$7 number) same (bus communiation protocol) and (designer engineer user developer) with (select$5 identif$6 specif$6 input$6 provid$6) and (port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)

	Search Text
1	bus adj protocol and communication adj protocol and port near2 (block component ip module device)
2	bus adj protocol and communication adj protocol and port near2 (block component ip module device) and compar$4 same protocol same (bus communication)
3	bus adj protocol and communication adj protocol and port near2 (block component ip module device) and compar$4 same protocol same (bus communication) and compatib$6 same protocol same (bus communication)
4	bus adj protocol and communication adj protocol and port near2 (block component ip module device) and compar$4 same protocol same (bus communication) and compatib$6 same protocol same (bus communication) and (user designer engineer)
5	bus near2 protocol and communication near protocol and port near2 (block component ip module device) and protocol with (bus communication) with compatib$6 and compatib$5 with (component device peripheral)
6	bus near2 protocol and communication near protocol and port near2 (block component ip module device) and protocol with (bus communication) with compatib$6 and compatib$5 with (component device peripheral) and support$4 with (component device peripheral)
7	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol
8	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and compatib$6 same port same (module block component ip device peripheral) and master and slave
9	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and (user designer programmer) same (cho$6 select$5) same (port width bus)

	Search Text
1	(bus communication) with protocol and port same (designer engineer user developer)
2	(select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
3	number same ((bus communication) with protocol) and port same (designer engineer user developer) same select$6 identif$7
4	(system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
5	port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
6	bus same port same (pld fpga module block component ip device peripheral) and port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
7	(compar$6 match$5) same (bus communication protocol) same (compati$6 common) and bus same port same (pld fpga module block component ip device peripheral) and port same (identif$6 select$6 protocol) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
8	(port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
9	(designer engineer user developer) with (select$5 identif$6 specif$6 input$6 provid$6) and (port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
10	(common compati$7 number) same (bus communiation protocol) and (designer engineer user developer) with (select$5 identif$6 specif$6 input$6 provid$6) and (port) same (connect$6 interconnect$4 communication) and (system bus) near3 (design$5) and (select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)
11	(system bus adj based) near3 design$4 same (bus communication) same protocol
12	12 and (designer engineer user developer) with (select$5 identif$6 specif$6 input$6 provid$6)
13	13 and (bus communication) same protocol same port same (coonect$6 interconnect$5)

	Search Text
1	bus adj based and port near3 (module block component ip device peripheral)
2	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) same (cho$6 select$5) same (port width bus protocol)
3	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) same (cho$6 select$5) same (port width bus protocol) and master and slave
4	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) with (present$5 enabl$5 cho$6 select$5) with (port width bus protocol) and master and slave
5	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol
6	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol and (user designer programmer engineer)
7	port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol and (user designer programmer engineer) and identif$7 with protocol
8	port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol and (user designer programmer engineer) and identif$7 with protocol with compar$7
9	port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol and (user designer programmer engineer) and identif$7 with protocol and single near5 bus and master and slave
10	system adj designer and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol and identif$7 with protocol and (user designer programmer engineer)
11	xml with protocol with (bus communication) with (standard known art widely commonly conventional) with (device component) with interfac$4 and @ad<"20030711"
12	xml with protocol with (bus communication) with (standard known art widely commonly conventional) with (device component) and @ad<"20030711" and "716"/$.ccls.
13	xml same protocol same (bus communication) same (standard known art widely commonly conventional) and @ad<"20030711"
14	xml with protocol with (bus communication) with (standard known art widely commonly conventional) with (device component) and @ad<"20030711"
15	xml with protocol with (bus communication) with (standard known art widely commonly conventional) and @ad<"20030711"
16	componet with port and component near2 library and protocol and bus and interfac$4 near3 (communication protocol bus)
17	(bus communication) with protocol and port same (designer engineer user developer)
18	(select$6 identif$7) same ((bus communication) with protocol) same port same (designer engineer user developer)

	Search Text
1	bus adj protocol and communication adj protocol and @ad<"20030711" and port near2 (block component ip module device)
2	bus adj protocol and communication adj protocol and @ad<"20030711" and port near2 (block component ip module device) and compar$4 same protocol same (bus communication)
3	bus adj protocol and communication adj protocol and @ad<"20030711" and port near2 (block component ip module device) and compar$4 same protocol same (bus communication) and compatib$6 same protocol same (bus communication)
4	bus adj protocol and communication adj protocol and @ad<"20030711" and port near2 (block component ip module device) and compar$4 same protocol same (bus communication) and compatib$6 same protocol same (bus communication) and (user designer engineer)
5	bus near2 protocol and communication near protocol and @ad<"20030711" and port near2 (block component ip module device) and protocol with (bus communication) with compatib$6 and compatib$5 with (component device peripheral)
6	bus near2 protocol and communication near protocol and @ad<"20030711" and port near2 (block component ip module device) and protocol with (bus communication) with compatib$6 and compatib$5 with (component device peripheral) and support$4 with (component device peripheral)
7	bus adj based and port near3 (module block component ip device peripheral)
8	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711"
9	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and compatib$6 same port same (module block component ip device peripheral)
10	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and compatib$6 same port same (module block component ip device peripheral) and master and slave
11	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) same (cho$6 select$5) same (port width bus)
12	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) same (cho$6 select$5) same (port width bus protocol)
13	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) same (cho$6 select$5) same (port width bus protocol) and master and slave
14	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and (user designer programmer) with (present$5 enabl$5 cho$6 select$5) with (port width bus protocol) and master and slave
15	bus adj based and port near3 (module block component ip device peripheral) and bus near2 protocol and communication near protocol and @ad<"20030711" and select$4 with protocol

	Search Text
1	bus adj width and (pld programmable adj logic adj device)
2	bus adj width and (pld programmable adj logic adj device) and (bus adj based adj design)
3	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word))
4	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word)) and @ad<="20030711"
5	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word)) and @ad<="20030711" and function$4 near3 represent$5 and system near3 component
6	bus adj width and (pld programmable adj logic adj device) and (bus adj protocol) and @ad<="20030711" and function$4 near3 represent$5 and system near3 component
7	bus near4 (width wide word) and (pld programmable adj logic adj device) and (bus adj protocol) and @ad<="20030711" and function$4 near3 represent$5 and system near3 component
8	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit wide word)) and @ad<="20030711" "."
9	bus adj width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word)) and @ad<="20030711" "."
10	bus near4 width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word)) and @ad<="20030711" "."
11	bus near4 width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word protocol)) and @ad<="20030711" "."
12	bus$3 near4 width and (pld programmable adj logic adj device fpga) and (bus$4 near5 (bit wide word protocol)) and @ad<="20030711" "."
13	bus$3 near4 width and bus near4 deisgn$4 and bus near4 protocol
14	bus$3 near4 width and bus near4 design$4 and bus near4 protocol
15	bus$3 near4 width and bus near4 design$4 and bus near4 protocol and (pld programmable adj logic adj device fpga)
16	bus$3 near4 width and bus near4 design$4 and bus near4 protocol and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5)
17	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near5 (compati$7 common)
18	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component
19	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near6 (compati$7 common) and port near3 name
20	bus$3 with (width (number near3 bit) wide) and bus near4 (communication protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near6 (allow$4 compati$7 common) and port near3 name
21	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) with component
22	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component